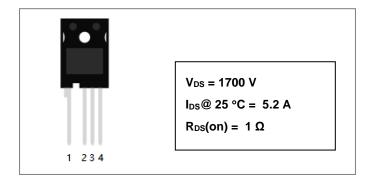
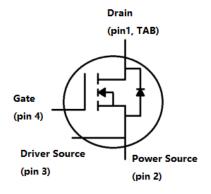




S1M1000170K 1700V Silicon Carbide Power MOSFET



Circuit Diagram



Description

S1M1000170K is a single SiC Power MOSFET packaged in a TO-247-4 case. The device is a high voltage n-channel enhancement mode MOSFET which has very low total conduction losses and very stable switching characteristics over temperature extremes. The S1M1000170K is ideal for energy sensitive, high frequency applications in challenging environments.

Features

- Positive temperature characteristics, easy to parallel.
- Low on-resistance typ. RDS(on) = 1 Ω .
- Fast switching speed and low switching losses.
- · Very fast and robust intrinsic body diode.
- · Process of non-bright tin electroplating.
- "-A" is an AEC-Q101 qualified device.

Applications

- EV Fast Charging Modules
- EV On-Board Chargers
- Solar Inverters
- Online UPS/Industrial UPS
- SMPS (Switch Mode Power Supplies)
- DC-DC Converters
- ESS (Energy Storage Systems)



RoHS

Maximum Ratings (T_A = 25 °C, unless otherwise specified)

Characteristics	Symbol	Conditions	Min.	Тур.	Max.	Units	Note
Drain - Source Voltage	V_{DSmax}	V _{GS} = 0 V, I _D = 100 μA			1700	V	
Gate - Source Voltage (dynamic)	V _{GSmax}	AC (f > 1 Hz)	-10		+25	V	
Gate - Source Voltage (static)	V_{GSop}	Static		-5 / +20		V	[1]
		V _{GS} = 20 V, T _C = 25 °C		5.2		٨	
Continuous Drain Current	I _D	V _{GS} = 20 V, T _C = 100 °C		3.7		A	
Pulsed Drain Current	I _{D(pulse)}	Pulse width t _P limited by T _{jmax}			15	А	
Power Dissipation	P _D	T _C = 25 °C			81	W	

^[1] Recommended turn off gate voltage is -5 V. Recommended turn on gate voltage is 20 V. Do not use with V_{GSON} < 15 V.





Electrical Characteristics (T_A = 25 °C, unless otherwise specified)

Characteristics	Symbol	Conditions	Min.	Тур.	Max.	Units
Drain Source Breakdown Voltage	V _{(BR)DSS}	V _{GS} = 0 V, I _D = 100 μA	1700			V
	$V_{GS(th)}$ $V_{DS} = V_{GS}, I_D = 0.5 \text{ mA}$ $V_{DS} = V_{GS}, I_D = 0.5 \text{ mA}, T_J = 175 °C$	$V_{DS} = V_{GS}$, $I_D = 0.5 \text{ mA}$	2	3.2	4	V
Gate Threshold Voltage			2.4		V	
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = 1700 V, V _{GS} = 0 V		1	100	μΑ
Gate Source Leakage Current	I _{GSS}	V _{GS} = 20 V, V _{DS} = 0 V		10	250	nA
Drain Source On-State	R _{DS(on)}	V _{GS} = 20 V, I _D = 2 A		1	1.3	Ω
Resistance		V _{GS} = 20 V, I _D = 2 A, T _J = 175 °C		1.9		Ω
Transconductance	gfs	V _{DS} = 20 V, I _{DS} = 2 A		1		S
		V _{DS} = 20 V, I _{DS} = 2 A, T _J = 175 °C		1.05		S
Input Capacitance	Ciss	V _G S = 0 V		160		
Output Capacitance	Coss	V _{DS} = 1000 V		10		pF
Reverse Transfer Capacitance	Crss	V _{AC} = 25 mV		2		
Coss Stored Energy	Eoss	f = 1 MHz		4		μJ
Turn-On Switching Energy	Eon	V _{DS} = 1000 V, V _{GS} = -5 / 20 V		55		
Turn-Off Switching Energy	Eoff	$I_D = 2 \text{ A}, R_{G(ext)} = 2.5 \Omega, L = 99 \text{ uH}$		24		μЈ
Turn-On Delay Time	t _{d(on)}	V _{DS} = 1000 V, V _{GS} = -5 / 20 V		4		
Rise Time	t _r	$I_D = 2 \text{ A}, R_{G(ext)} = 2.5 \Omega$		6		ns

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Turn-Off Delay Time	$t_{d(off)}$	Inductive Load Timing relative to		8		
Fall Time	t _f	VDS Per IEC60747-8-4 pg 83		77		
Internal Gate Resistance	R _{G(int)}	f = 1 MHz, AC = 25 mV		20		Ω
Gate to Source Charge	Qgs	V _{DS} = 1700 V, V _{GS} = -5 / 20 V		2.5		
Gate to Drain Charge	Q_{gd}	I _D = 2 A		5.2		nC
Total Gate Charge	Qg	Per IEC60747-8-4 pg 21		10		

Reverse Diode Characteristics (T_A = 25 °C, unless otherwise specified)

Characteristics	Symbol	Conditions	Тур.	Max.	Units
Diada Farward Valtaga	V_{SD}	V _{GS} = -5 V, I _{SD} = 1 A	4.6		V
Diode Forward Voltage	V_{SD}	V _{GS} = -5 V, I _{SD} = 1 A, T _J = 175 °C	4.3		V
Continuous Diode Forward Current	Is	V _{GS} = -5 V, T _C = 25 °C	9		А
Reverse Recovery Time	t _{rr}	V _{GS} = -5 V, I _{SD} = 2 A, T _J = 25 °C	6		ns
Reverse Recovery Charge	Q_{rr}	V _R = 1200 V	25		nC
Peak Reverse Recovery Current	I _{mm}	dif / dt = 2500 A / μs	7		А

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Thermal-Mechanical Specifications

Characteristics	Symbol	Condition	Specification	Units
Junction Temperature	TJ	-	-55 to +175	°C
Storage Temperature	T _{stg}	-	-55 to +175	°C
Typical Thermal Resistance Junction to Case	R _θ JC	DC operation	1.85	°C / W

Ordering Information

Device	Package	Shipping	
S1M1000170K	TO-247-4	30pcs / tube	

Marking Diagram



Where XXXXX is YYWWL

S1M = Device Type1000 = $R_{DS}(on)$

170 = Reverse Voltage (1700V)

K = Package
 SSG = SSG
 YY = Year
 WW = Week
 L = Lot Number

Cautions: Molding resin

Epoxy resin UL:94V-0





Ratings and Characteristics Curves

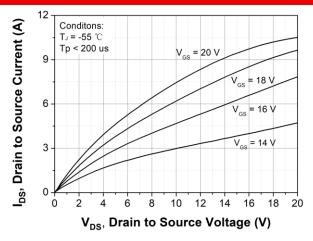


Figure 1. Output Characteristics T_J = -55 °C

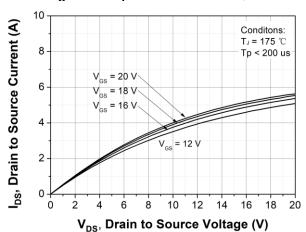


Figure 3. Output Characteristics T_J = 175 °C

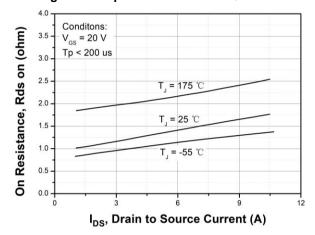


Figure 5. On-Resistance vs. Drain Current For Various Temperatures

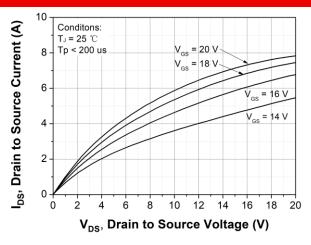


Figure 2. Output Characteristics T_J = 25 °C

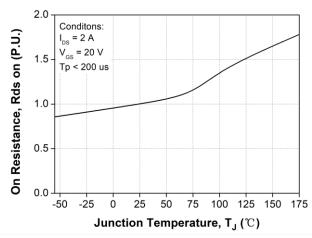


Figure 4. Normalized On-Resistance vs. Temperature

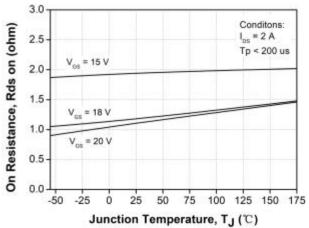


Figure 6. On-Resistance vs. Temperature For Various Gate Voltage

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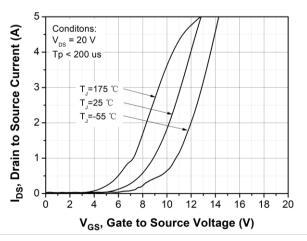


Figure 7. Transfer Characteristic for Various Junction Temperatures

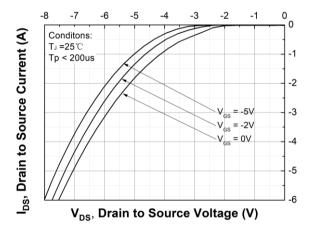


Figure 9. Body Diode Characteristic at T_J = 25 °C

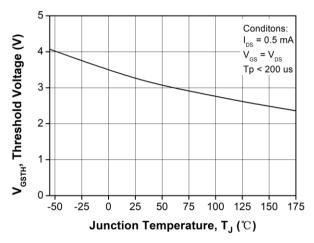


Figure 11. Threshold Voltage vs. Temperature



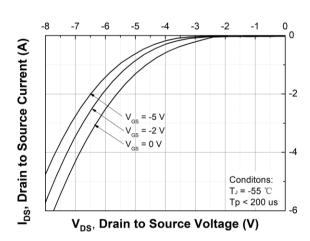


Figure 8. Body Diode Characteristic at T_J = -55 °C

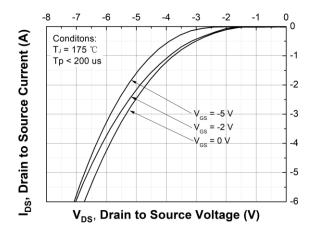


Figure 10. Body Diode Characteristic at T_J = 175 °C

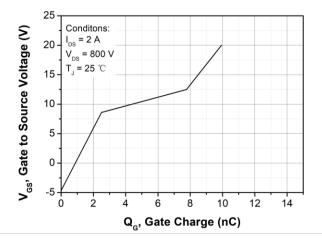


Figure 12. Gate Charge Characteristic

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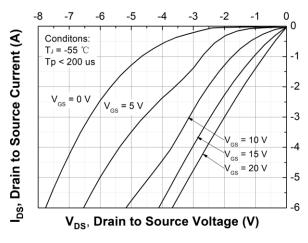


Figure 13. 3rd Quadrant Characteristic at T_J = -55 °C

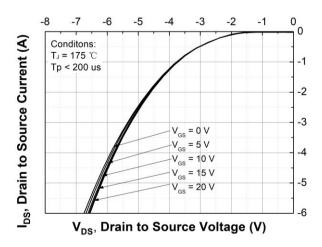


Figure 15. 3rd Quadrant Characteristic at T_J = 175 °C

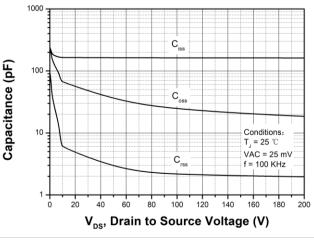


Figure 17. Capacitances vs. Drain-Source Voltage (0 - 200 V)

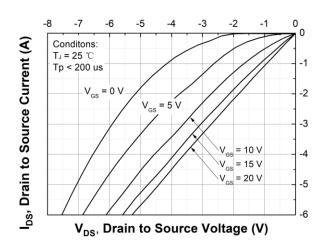


Figure 14. 3rd Quadrant Characteristic at T_J = 25 °C

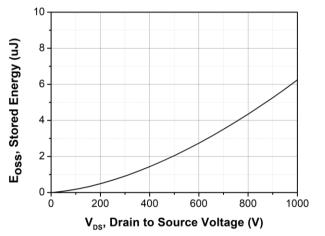


Figure 16. Output Capacitor Stored Energy

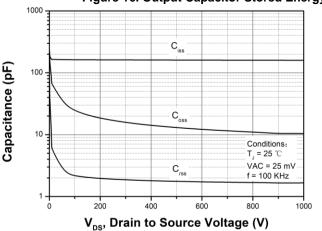


Figure 18. Capacitances vs. Drain-Source Voltage (0 - 1000 V)

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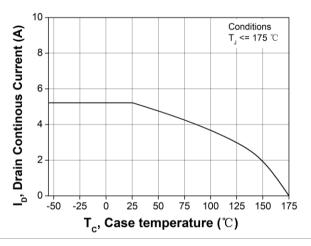


Figure 19. Continuous Drain Current Derating vs.

Case Temperature

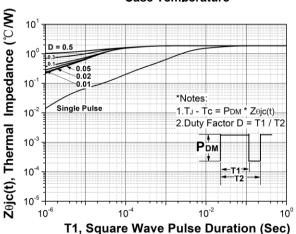


Figure 21. Transient Thermal Impedance (Junction - Case)

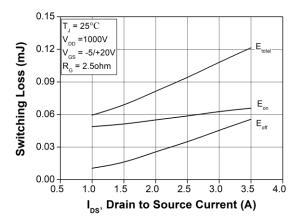


Figure 23. Clamped Inductive Switching Energy vs. Drain Current (V_{DD} = 600V)

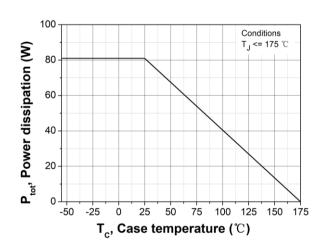


Figure 20. Maximum Power Dissipation Derating vs. Case Temperature

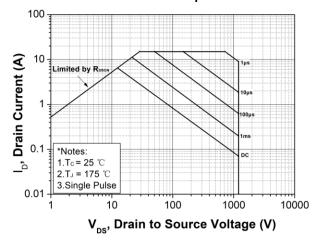


Figure 22. Safe Operating Area

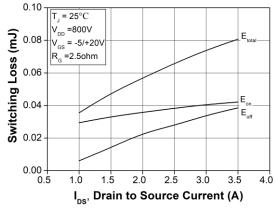


Figure 24. Clamped Inductive Switching Energy vs. Drain Current ($V_{DD} = 800V$)

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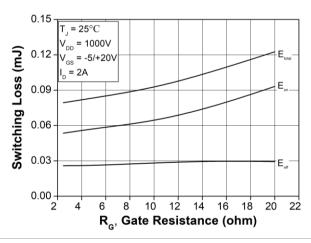


Figure 25. Clamped Inductive Switching Energy vs. R_{G(ext)}

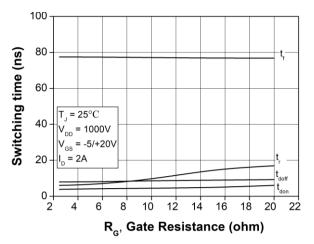


Figure 27. Switching Times vs. R_{G(ext)}

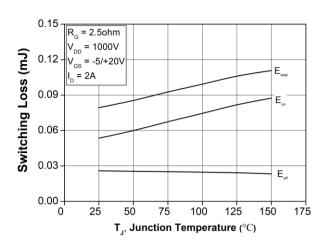


Figure 26. Clamped Inductive Switching Energy vs.
Temperature

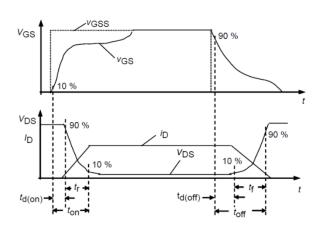
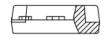


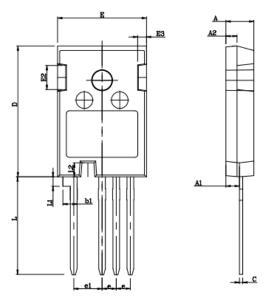
Figure 28. Switching Times Definition

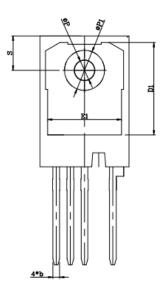




Mechanical Dimensions TO-247-4







mm						
Min	Nom	Max				
4.80	5.00	5.20				
2.23	2.41	2.59				
1.85	2.00	2.15				
1.11	1,21	1.36				
2.35	2.55	2.75				
0.51	0.61	0.75				
23.30	23.45	23.60				
16.25	16.55	16.85				
15.75	15.94	16.10				
13.00	13.26	13.43				
4.00	4.30	4.60				
1.15	1.45	1.75				
	2.54BSC					
5.08BSC						
17.31	17.47	17.82				
1.50	1.70	1.90				
3.51	3.60	3.65				
7.08	7.19	7.30				
6.15BSC						
	4.80 2.23 1.85 1.11 2.35 0.51 23.30 16.25 15.75 13.00 4.00 1.15	Min Nom 4.80 5.00 2.23 2.41 1.85 2.00 1.11 1.21 2.35 2.55 0.51 0.61 23.30 23.45 16.25 16.55 15.75 15.94 13.00 13.26 4.00 4.30 1.15 1.45 2.54BSC 5.08BSC 17.31 17.47 1.50 1.70 3.51 3.60 7.08 7.19				

S1M1000170K



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